

Claims

1 1. A metrology target mask for determining proper lithographic exposure dose
2 and/or focus in a pattern formed in a layer on a semiconductor substrate by lithographic
3 processing comprising:

4 a mask substrate;

5 a first, dose and focus sensitive mask portion on the mask substrate having a first
6 array of elements comprising a plurality of spaced, substantially parallel
7 elements having essentially the same length and width, ends of the individual
8 elements being aligned to form first and second opposing array edges, the
9 lengths of and spaces between said elements being sensitive to both dose and
10 focus of an energy beam when lithographically printed in a layer on a
11 semiconductor substrate; and

12 a second, dose sensitive mask portion on the mask substrate having a second array
13 of elements comprising a central element having a length and a width, and a
14 plurality of spaced, substantially parallel outer elements having a length and a
15 width, the width of the outer elements being less than the width of the central
16 element, edges of outer elements on each side of and farthest from the central
17 element forming opposing array edges, the pitch of said outer elements being
18 selected such that the outer elements are not resolvable after lithographic
19 printing in a layer on a semiconductor substrate and the resulting printed second
20 target portion width is sensitive to dose but not focus of the energy beam,

21 wherein, after projecting an energy beam through the mask and lithographically
22 printing the mask portions in a layer on a semiconductor substrate and determining the
23 widths of the first and second target portions in the layer by measuring distance
24 between opposing array edges in each of the first and second portions, dose and/or
25 focus of the energy beam during lithographic processing of said layer may be
26 determined.

1 2. The mask of claim 1 wherein the edges of the array in the first mask portion are
2 substantially parallel to edges of the array in the second mask portion, and the elements
3 in the first mask portion are substantially perpendicular to the elements in the second
4 mask portion.

1 3. The mask of claim 1 wherein pitch between elements of the arrays in the second
2 mask sensitive portion is less than the resolution limit of the energy beam in the
3 imaging system used to expose the mask in the lithographic processing.

1 4. The mask of claim 1 wherein the second, dose sensitive mask portion includes a
2 plurality of outer elements on each side of said central element, the width of the outer
3 elements decreasing with distance from the central element.

1 5. The mask of claim 1 wherein the elements of the first and second mask portions
2 comprises opaque elements on a substantially transparent mask substrate.

1 6. The mask of claim 1 wherein the elements of the first and second mask portions
2 comprises substantially transparent elements on an opaque mask substrate.

1 7. A metrology target mask for determining proper lithographic exposure dose
2 and/or focus in a pattern formed in a layer on a semiconductor substrate by lithographic
3 processing comprising:

- 4 a mask substrate;
- 5 a first, dose and focus sensitive mask portion on the mask substrate having a first
- 6 array of elements comprising a plurality of spaced, substantially parallel
- 7 elements having essentially the same length and width, ends of the individual
- 8 elements being aligned to form first and second opposing array edges, the

9 lengths of and spaces between said elements being sensitive to both dose and
10 focus of an energy beam when lithographically printed in a layer on a
11 semiconductor substrate; and

12 a second, dose sensitive mask portion on the mask substrate having a second array
13 of elements comprising a central element having a length and a width, and a
14 plurality of spaced, substantially parallel outer elements having a length and a
15 width, the outer elements being substantially perpendicular to the central
16 element, ends of the outer elements farthest from the central element being
17 aligned to form first and second opposing array edges, the pitch of said outer
18 elements being selected such that the outer elements are not resolvable after
19 lithographic printing in a layer on a semiconductor substrate and the resulting
20 printed second target portion width is sensitive to dose but not focus of the
21 energy beam,

22 wherein, after projecting an energy beam through the mask and lithographically
23 printing the mask portions in a layer on a semiconductor substrate and determining the
24 widths of the first and second target portions in the layer by measuring distance
25 between opposing array edges in each of the first and second portions, dose and/or
26 focus of the energy beam during lithographic processing of said layer may be
27 determined.

1 8. The mask of claim 7 wherein the edges of the array in the first mask portion are
2 substantially parallel to edges of the array in the second mask portion, and the elements
3 in the first mask portion are substantially perpendicular to the outer elements in the
4 second mask portion.

1 9. The mask of claim 7 wherein pitch between outer elements of the arrays in the
2 second dose sensitive mask portion is less than the resolution limit of the energy beam
3 in the imaging system used to expose the mask in the lithographic processing.

1 11. A metrology target for determining proper lithographic exposure dose and/or
2 focus in a pattern formed in a layer on a semiconductor substrate by lithographic
3 processing comprising:

4 a substrate;

5 a first, dose and focus sensitive target portion in a lithographically formed layer on
6 the substrate having a first array of elements comprising a plurality of spaced,
7 substantially parallel elements having essentially the same length and width,
8 ends of the individual elements being aligned to form first and second opposing
9 array edges, the lengths of and spaces between said elements being sensitive to
10 both dose and focus of an energy beam when lithographically printed in a layer
11 on a semiconductor substrate; and

a second, dose sensitive target portion in the lithographically formed layer on the substrate having a single element having a length and a width, edges along the length of the single element forming opposing array edges, the width of the single element being sensitive to dose but not focus of the energy beam when lithographically printed in a layer on a semiconductor substrate,

17 wherein, after determining the widths of the first and second target portions in the layer
18 by measuring distance between opposing array edges in each of the first and second
19 portions, dose and/or focus of the energy beam used during lithographic processing of
20 said layer may be determined.

1 12. The target of claim 11 wherein the edges of the array in the first target portion
2 are substantially parallel to edges of the array in the second target portion, and the

3 elements in the first target portion are substantially perpendicular to the elements in the
4 second target portion.

1 13. The target of claim 11 wherein the spacing between elements of the arrays in the
2 first target portion is less than the resolution limit of the energy beam in the imaging
3 system used to expose the mask in the lithographic processing.

1 14. The target of claim 11 wherein the substrate is electrically non-conductive, the
2 elements in each of the first and second target portions are electrically conductive, and
3 the elements in the first array are electrically connected, and wherein, upon applying a
4 current across each of the arrays and measuring the voltage, the suitability of the layer
5 may be determined by the resistance of each of the arrays.

1 15. The target of claim 14 wherein each target portion has a first end and a second
2 end, the target portions being electrically connected at the second ends, and including
3 electrically conductive pads at the first end of each of the target portions and the
4 connected second ends of the target portions such that current may be applied between
5 the pads on the target portion first ends and voltage may be measured between the first
6 and second ends of each of the targets.

1 16. The target of claim 15 wherein the elements in the first target portion are
2 electrically connected by an electrically conductive central element, and wherein the
3 individual elements of the first target portion extend in a perpendicular direction from
4 each side of the first target portion central element.

1 17. A method of determining proper lithographic exposure dose and/or focus in a
2 pattern formed in a layer on a semiconductor substrate by lithographic processing, the
3 method comprising:

4 providing a semiconductor substrate;
5 providing the metrology target mask of claim 1;
6 projecting an energy beam through the mask onto the semiconductor substrate;
7 lithographically forming a target in a layer on the semiconductor substrate having
8 first and second target portions corresponding to the first and second mask
9 portions, respectively;
10 determining the widths of the first and second target portions in the layer by
11 measuring distance between opposing array edges in each of the first and second
12 portions; and
13 using the measured widths of the first and second target portions in the layer to
14 determine dose and/or focus of the energy beam used during lithographic
15 processing of said layer.

1 18. The method of claim 17 wherein the elements of the second, dose sensitive mask
2 portion are not resolved by the energy beam in forming the corresponding second target
3 portion, and wherein the second target portion comprises a single element formed in the
4 layer on the semiconductor substrate having a length and a width, with edges along the
5 length of the single element forming opposing array edges.

1 19. The method of claim 18 wherein the edges of the array in the first target portion
2 are substantially parallel to edges of the array in the second target portion, and the
3 elements in the first target portion are substantially perpendicular to the element in the
4 second target portion.

1 20. A method of determining proper lithographic exposure dose and/or focus in a
2 pattern formed in a layer on a semiconductor substrate by lithographic processing, the
3 method comprising:
4 providing a semiconductor substrate;

5 providing the metrology target mask of claim 7;
6 projecting an energy beam through the mask onto the semiconductor substrate;
7 lithographically forming a target in a layer on the semiconductor substrate having
8 first and second target portions corresponding to the first and second mask
9 portions, respectively;
10 determining the widths of the first and second target portions in the layer by
11 measuring distance between opposing array edges in each of the first and second
12 portions; and
13 using the measured widths of the first and second target portions in the layer to
14 determine dose and/or focus of the energy beam used during lithographic
15 processing of said layer.

1 21. The method of claim 20 wherein the elements of the second, dose sensitive mask
2 portion are not resolved by the energy beam in forming the corresponding second target
3 portion, and wherein the second target portion comprises a single element formed in the
4 layer on the semiconductor substrate having a length and a width, with edges along the
5 length of the single element forming opposing array edges.

1 22. A method of electrically testing image shortening of a pattern formed on a
2 substrate by lithographic or etch processing comprising:
3 providing the electrically conductive target of claim 17;
4 applying a current across the first and second target portions;
5 measuring the voltage drop and determining the resistance across each of the first
6 and second target portions; and
7 determining the suitability of the layer by the resistance of the first and second
8 target portions.

